LAA110 Dual Single-Pole, Normally Open OptoMOS® Relay

| Parameter | Ratings | Units |
| :--- | :---: | :---: |
| Blocking Voltage | 350 | $\mathrm{~V}_{\mathrm{P}}$ |
| Load Current | 120 | $\mathrm{~mA}_{\mathrm{rms}} / \mathrm{mA}_{\mathrm{DC}}$ |
| On-Resistance (max) | 35 | $\Omega$ |

## Features

- 3750V ${ }_{\text {rms }}$ Input/Output Isolation
- Low Drive Power Requirements (TTL/CMOS Compatible)
- Arc-Free With No Snubbing Circuits
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable
- Surface Mount Tape \& Reel Packages Available


## Applications

- Telecom Switching
- Tip/Ring Circuits
- Modem Switching (Laptop, Notebook, Pocket Size)
- Hook Switch
- Dial Pulsing
- Ground Start
- Ringing Injection
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls


## Description

LAA110 is a dual 1-Form-A Solid State Relay that has two independently controlled, optically coupled MOSFET switches. The output MOSFET switches and photovoltaic die employ optically coupled MOSFET technology to provide $3750 \mathrm{~V}_{\text {rms }}$ of input-to-output isolation.

The relay outputs, that use patented OptoMOS architecture, are controlled by a highly efficient GaAIAS infrared LED.

This dual pole OptoMOS relay provides a more compact design solution than discrete single-pole relays in a variety of applications, and saves board space by incorporating both switches in a single 8-Pin package.

## Approvals

- UL Certified Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950-1 Certified Component:

TUV Certificate B 100549410006

## Ordering Information

| Part \# | Description |
| :--- | :--- |
| LAA110 | 8-Pin DIP (50/Tube) |
| LAA110S | 8-Pin Surface Mount (50/Tube) |
| LAA110STR | 8-Pin Surface Mount (1,000/Reel) |
| LAA110P | 8-Pin Flat Pack (50/Tube) |
| LAA110PTR | 8-Pin Flat Pack (1,000/Reel) |

## Pin Configuration



Switching Characteristics of Normally Open (Form A) Devices


Absolute Maximum Ratings @ $25^{\circ} \mathrm{C}$

| Parameter | Ratings | Units |
| :--- | :---: | :---: |
| Blocking Voltage | 350 | $\mathrm{~V}_{\mathrm{p}}$ |
| Reverse Input Voltage | 5 | V |
| Input Control Current | 50 | mA |
| Peak (10ms) | 1 | A |
| Input Power Dissipation ${ }^{1}$ | 150 | mW |
| Total Power Dissipation ${ }^{2}$ | 800 | mW |
| Isolation Voltage, Input to Output | 3750 | $\mathrm{~V}_{\text {rms }}$ |
| Operational Temperature | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | -40 to +125 | ${ }^{\circ} \mathrm{C}$ |

C Derate linearly $1.33 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$
2 Derate linearly $6.67 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

## Electrical Characteristics @ $25^{\circ} \mathrm{C}$

| Parameter | Conditions | Symbol | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output Characteristics |  |  |  |  |  |  |
| Load Current, Continuous ${ }^{1}$ | - | $\mathrm{I}_{\mathrm{L}}$ | - | - | 120 | $\mathrm{mA}_{\text {rms }} / \mathrm{mA} \mathrm{A}_{\text {d }}$ |
| Peak Load Current | $\mathrm{t}=10 \mathrm{~ms}$ | $\mathrm{I}_{\text {LPK }}$ | - | - | $\pm 350$ | $\mathrm{mA}_{\mathrm{p}}$ |
| On-Resistance ${ }^{2}$ | $\mathrm{I}_{\mathrm{L}}=120 \mathrm{~mA}$ | $\mathrm{R}_{\mathrm{ON}}$ | - | 25 | 35 | $\Omega$ |
| Off-State Leakage Current | $\mathrm{V}_{\mathrm{L}}=350 \mathrm{~V}_{\mathrm{P}}$ | $\mathrm{I}_{\text {LEAK }}$ | - | - | 1 | $\mu \mathrm{A}$ |
| Switching Speeds Turn-On |  | $\mathrm{t}_{\text {on }}$ | - | - | 3 | ms |
| Turn-Off | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=10 \mathrm{~V}$ | $\mathrm{t}_{\text {off }}$ | - | - | 3 | ms |
| Output Capacitance | $\mathrm{V}_{\mathrm{L}}=50 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ | $\mathrm{C}_{\text {OUT }}$ | - | 25 | - | pF |
| Input Characteristics |  |  |  |  |  |  |
| Input Control Current to Activate | $\mathrm{I}_{\mathrm{L}}=120 \mathrm{~mA}$ | $I_{\text {F }}$ | - | - | 5 | mA |
| Input Control Current to Deactivate | - | - | 0.4 | 0.7 | - | mA |
| Input Voltage Drop | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}$ | $V_{F}$ | 0.9 | 1.2 | 1.4 | V |
| Reverse Input Current | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}$ | $I_{\text {R }}$ | - | - | 10 | $\mu \mathrm{A}$ |
| Common Characteristics |  |  |  |  |  |  |
| Input to Output Capacitance | - | $\mathrm{C}_{10}$ | - | 3 | - | pF |

${ }^{1}$ If both poles operate the load current must be derated so as not to exceed the package power dissipation value.
${ }^{2}$ Measurement taken within 1 second of on-time.

PERFORMANCE DATA @ $25^{\circ} \mathrm{C}$ (Unless Otherwise Noted)*






Typical Blocking Voltage Distribution ( $\mathrm{N}=50$ )




*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA @ $25^{\circ} \mathrm{C}$ (Unless Otherwise Noted)*

*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

## Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) rating as shown below, and should be handled according to the requirements of the latest version of the joint industry standard IPC/JEDEC J-STD-033.

| Device | Moisture Sensitivity Level (MSL) Rating |
| :---: | :---: |
| LAA110 / LAA110S / LAA110P | MSL 1 |

## ESD Sensitivity

This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

## Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of J-STD-020 must be observed.

| Device | Maximum Temperature x Time |
| :---: | :---: |
| LAA110 / LAA110S | $250^{\circ} \mathrm{C}$ for 30 seconds |
| LAA110P | $260^{\circ} \mathrm{C}$ for 30 seconds |

## Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

MECHANICAL DIMENSIONS

## LAA110


$\frac{\text { Dimensions }}{\mathrm{mm}}$ (inches)

## LAA110S



## PCB Land Pattern




## LAA110P



PCB Land Pattern


## MECHANICAL DIMENSIONS

## LAA110STR Tape \& Reel


2. Tape complies with all "Notes" for constant dimensions listed on page 5 of EIA-481-2

## LAA110PTR Tape \& Reel



## For additional information please visit our website at: www.ixysic.com

$\overline{\text { IXYS Integrated Circuits Division makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication and reserves the right to make }}$ changes to specifications and product descriptions at any time without notice. Neither circuit patent licenses nor indemnity are expressed or implied. Except as set forth in IXYS Integrated Circuits Division's Standard Terms and Conditions of Sale, IXYS Integrated Circuits Division assumes no liability whatsoever, and disclaims any express or implied warranty, relating to its products including, but not limited to, the implied warranty of merchantability, fitness for a particular purpose, or infringement of any intellectual property right.

The products described in this document are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or where malfunction of IXYS Integrated Circuits Division's product may result in direct physical harm, injury, or death to a person or severe property or environmental damage. IXYS Integrated Circuits Division reserves the right to discontinue or make changes to its products at any time without notice.

## Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery \& Lifecycle Information:

IXYS:
LAA110P LAA110S LAA110PTR LAA110 LAA110STR

