

Clock Oscillator

TTL / HCMOS, TRISTATE

Standard Specifications

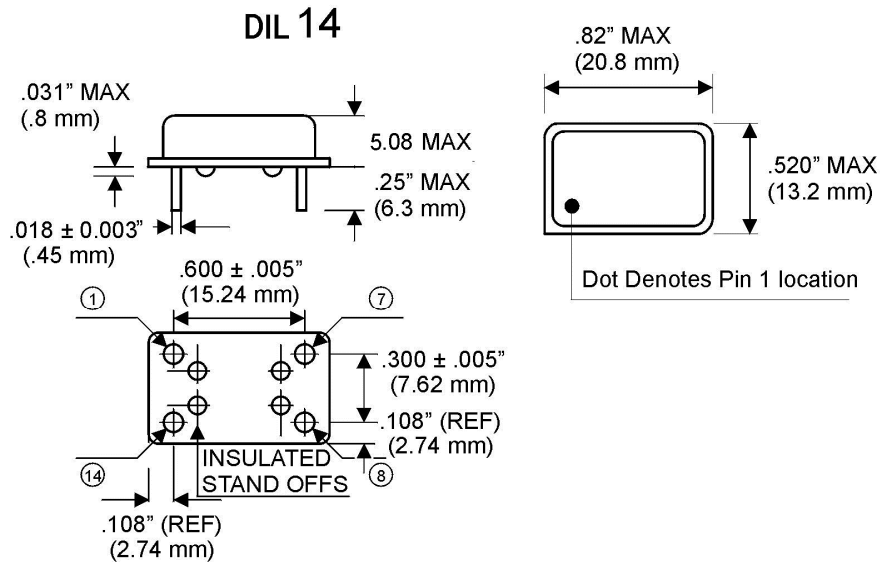
item	Tri-State															
model	KXO-400															
package	DIL 14															
frequency range	0,5 ~ 100 MHz															
frequency stability	standard ± 100 ppm available ± 25 ppm ~ ± 100 ppm															
storage temperature range	-55°C ~ +125°C															
operating temperature range	standard -20°C ~ + 70°C available -40°C ~ + 85°C (=KXO-405)															
symmetry	standard 60/40% (+1/2V _{DD}) available 55/45%															
rise and fall time max.	10 ns 0.5 ~ 25 MHz 6 ns 25.1 ~ 70 MHz 4 ns 70.1 ~ 100 MHz															
"O" level (max.)	+0.5V (10%V _{DD})															
"1" level (min.)	+4.5V (90%V _{DD})															
input voltage	+5.0V DC ± 10%															
input current max.	20mA 0.5 ~ 20MHz 40mA 20.1 ~ 70MHz 60mA 70.1 ~ 100MHz															
output load	1 - 8TTL or CL = 50pF (TYP)															
logic family	TTL or HCMOS compatible															
test circuit	fig. 3, 4															
pin connection	<table><tr><td>PIN</td><td colspan="2">CONNECTION</td></tr><tr><td>#1 (#1)</td><td>„L“(OV)</td><td>„H“(+5V) or OPEN</td></tr><tr><td>#7 (#4)</td><td colspan="2">GND</td></tr><tr><td>#8 (#5)</td><td>Z</td><td>OUTPUT</td></tr><tr><td>#14 (#8)</td><td colspan="2">V_{DD}</td></tr></table>	PIN	CONNECTION		#1 (#1)	„L“(OV)	„H“(+5V) or OPEN	#7 (#4)	GND		#8 (#5)	Z	OUTPUT	#14 (#8)	V _{DD}	
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#7 (#4)	GND															
#8 (#5)	Z	OUTPUT														
#14 (#8)	V _{DD}															
packing unit	25 pcs.															
part no.	12.xxxxx															

Z: high impedance

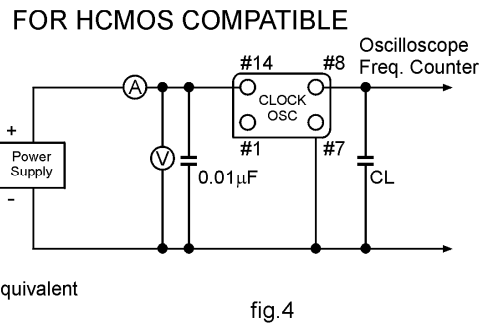
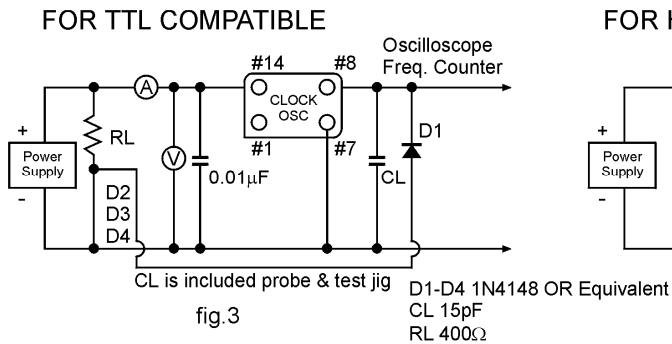
Enable/Disable Phase
Delay Time 100 ns max.



Dimensions (mm):



Test Circuit:



Output Wave Shape:

