

# CPLD TRAINER KIT

## Xilinx XC9572XL



### Introduction and Features:

- The CPLD Development kit based on Xilinx XC9572XL is a low cost evaluation platform for training, testing and developing designs based on the Xilinx 9500XL family of CPLD.
- The board contains the 1600 gate, 72 macro cells, 100 Pin XC9572XL CPLD with 72 user I/O's.
- An on-board power supply includes 3.3V regulator, which regulates Vcc internal & Vcc I/O for the CPLD.
- A 40 MHz Crystal Oscillator provides clock source that is directly connected to GCK1 clock input on the 9572XL device.
- All user I/O pins are brought out of the 9572XL device.
- Peripheral interfaces like LEDs, 7 Segment Displays, Key Switches & Buzzer are provided on board.
- Separate Jumper settings have been provided for accessing the I/Os for other external applications.
- A built-in JTAG programmer board is also available for programming the XC9572XL.
- The HDL code can easily be implemented using the Webpack software available on the Xilinx Website.
- Datasheets are provided in the CD-ROM accompanying the Kit and a softcopy of the user manual is also included in the CD as well.

### Packaging

Your package contains

1. CPLD kit
2. JTAG Parallel port cable
3. User manual
4. CD containing software (XILINX ISE webpack)