Electronics

## Features

- Low Power Consumption: < $15 \mu \mathrm{~A} @+2.5 \mathrm{~V}$
- High Isolation: 50 dB Typical @ 2 GHz
- Low Insertion Loss: 0.7 dB @ 2 GHz
- Positive 2.5 to 5 V Control
- Low Cost Plastic MSOP-10 Package


## Description

M/A-COM's SW-439 is a GaAs MMIC SPDT switch in a low cost MSOP-10 surface mount plastic package. This part is ideal for high isolation, broadband switching requirements. Typical applications include synthesizer switching, transmit/ receive switching, switch matrices and filter banks in systems such as radio and cellular equipment, PCM, GPS, and fiber optic modules.

The SW-439 is fabricated as a monolithic GaAs MMIC using a 0.5 micron PHEMT process. The process features full passivation.

## Ordering Information

| Part Number | Package |
| :---: | :---: |
| SW-439 | Bulk Packaging |
| SW-439TR | 1000 piece reel |
| SW-439SMB | Sample Board |

Note: Reference Application Note M513 for reel size information.

## Absolute Maximum Ratings ${ }^{1,2}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| Input Power | +30 dBm |
| Operating Voltage | +8.5 Volts |
| Operating Temperature | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |

1. Exceeding any one or combination of these limits may cause permanent damage to this device.
2. $M / A-C O M$ does not recommend sustained operation near these survivability limits.

## Functional Schematic ${ }^{3}$


3. For improved performance at frequencies below 500 MHz , use larger value capacitors.

## Pin Configuration

| Pin No. | Function | Pin No. | Function |
| :---: | :---: | :---: | :---: |
| 1 | Control 1 | 6 | RF Port 2 |
| 2 | Ground | 7 | Ground |
| 3 | RF Input | 8 | Ground |
| 4 | Ground | 9 | Ground |
| 5 | Control 2 | 10 | RF Port 1 |

## Truth Table ${ }^{4,5}$

| Control V1 | Control V2 | RFC - RF1 | RFC - RF2 |
| :---: | :---: | :---: | :---: |
| 0 | 1 | Off | On |
| 1 | 0 | On | Off |

4. External DC blocking capacitors are required on all RF ports.
5. " 0 " $=0 \pm 0.2 \mathrm{Vdc}, " 1 "=+2.5$ to +5 Vdc

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Visit www.macom.com for additional data sheets and product information.

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Electrical Specifications: $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{C}}=0 / 3 \mathrm{~V}, \mathrm{Z}_{0}=50 \Omega$

| Parameter | Test Conditions | Units | Min | Typ | Max |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | $\begin{aligned} & \mathrm{DC}-1.0 \mathrm{GHz} \\ & 1.0-2.0 \mathrm{GHz} \\ & 2.0-3.0 \mathrm{GHz} \end{aligned}$ | dB <br> dB <br> dB | — | $\begin{aligned} & 0.55 \\ & 0.65 \\ & 0.80 \end{aligned}$ | $\begin{aligned} & 0.65 \\ & 0.75 \\ & 0.90 \end{aligned}$ |
| Isolation | $\begin{aligned} & \mathrm{DC}-2.0 \mathrm{GHz} \\ & 2.0-3.0 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \end{aligned}$ | $\begin{aligned} & 45 \\ & 31 \end{aligned}$ | $\begin{aligned} & 47 \\ & 33 \end{aligned}$ | - |
| VSWR | 0.25-3.0GHz | Ratio | - | 1.2:1 | 1.3:1 |
| P1dB | $500 \mathrm{MHz}-2.0 \mathrm{GHz}, \mathrm{V}_{\mathrm{C}}=2.5 \mathrm{~V}$ | dBm | - | 20 | - |
| P1dB | $500 \mathrm{MHz}-2.0 \mathrm{GHz}, \mathrm{V}_{\mathrm{C}}=5 \mathrm{~V}$ | dBm | - | 28 | - |
| IP2 | 2 Tone, $900 \mathrm{MHz}, 5 \mathrm{MHz}$ Spacing, $\mathrm{V}_{\mathrm{C}}=3 \mathrm{~V}$ | dBm | - | 85 | - |
| IP3 | 2 Tone, $900 \mathrm{MHz}, 5 \mathrm{MHz}$ Spacing, $\mathrm{V}_{\mathrm{C}}=3 \mathrm{~V}$ | dBm | - | 50 | - |
| Ton, Toff | 50\% Control to 90\% RF, 50\% Control to 10\% RF | nS | - | 20 | - |
| Trise, Tfall | 10\% to $90 \%$ RF, $90 \%$ to $10 \%$ RF | nS | - | 10 | - |
| Transients | In-band | mV | - | 15 | - |
| Control Current | $\left\|\mathrm{V}_{\mathrm{C}}\right\|=2.5 \mathrm{~V}$ | $\mu \mathrm{A}$ | - | 5 | 15 |

## MSOP-10



NOTES:

1. REFERENCE JEDEC MO-187-BA, FOR ADDITIONAL DIMENSIONAL AND
TOLERANCE ENFRMATION.

2. ALL DIMENSIONS SHOWN AS INCHESIMM.

## Handling Procedures

Please observe the following precautions to avoid damage:

## Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

## Typical Performance Curves

## Insertion Loss



VSWR


## Isolation



