Advance Information The RF Small Signal Line Silicon Lateral FET N-Channel Enhancement-Mode MOSFET

Designed for use in low voltage, moderate power amplifiers such as portable analog and digital cellular radios and PC RF modems.

- Performance Specifications at 5.8 V, 900 MHz: Output Power = 30 dBm Min Power Gain = 10 dB Typ Efficiency = 50% Min
- Guaranteed Ruggedness at Load VSWR = 20:1
- New Plastic Surface Mount Package
- Available in Tape and Reel Packaging. T1 Suffix = 1,000 Units per 8 mm, 7 inch Reel
- Device Marking = 9745

MRF9745T1

30 dBm, 900 MHz HIGH FREQUENCY POWER TRANSISTOR LDMOS FET



CASE 449-02, STYLE 1 (PLD-1)

MAXIMUM RATINGS

Rating		Symbol	Val	ue	Unit
Drain–Source Voltage		V _{DSS}	3	5	Vdc
Drain–Gate Voltage (R_{GS} = 1 M Ω)		V _{DGO}	2	5	Vdc
Gate-Source Voltage		V _{GS}	±10		Vdc
Drain Current – Continuous		۱ _D	2		Adc
Total Device Dissipation @ T _C = 50°C Derate above 50°C		PD	10 100		W mW/°C
Storage Temperature Range		T _{stg}	- 65 to +150		°C
Operating Temperature Range		ТJ	150		°C
THERMAL CHARACTERISTICS					
Characteristic		Symbol	Max		Unit
Thermal Resistance, Junction to Case		R _{θJC}	10		°C/W
ELECTRICAL CHARACTERISTICS (T _C = 25°C unless otherwis	se noted)				
Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•				
Drain–Source Leakage Current $(V_{DS} = 35 \text{ V}, V_{GS} = 0)$	IDSS	-	-	10	μAdc
Gate–Source Leakage Current $(V_{GS} = 5 \text{ V}, V_{DS} = 0)$	IGSS	-	-	1	μAdc

NOTE – **<u>CAUTION</u>** – MOS devices are susceptible to damage from electrostatic charge. Reasonable precautions in handling and packaging MOS devices should be observed.



ELECTRICAL CHARACTERISTICS – continued ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS		•	•		
Gate Threshold Voltage $(V_{DS} = 6 \text{ V}, \text{ I}_{D} = 25 \mu\text{A})$	VGS(th)	1	2	3	Vdc
Forward Transconductance $(V_{DS} = 6 \text{ V}, I_D = 200 \text{ mA})$	9fs	-	550	-	mmhos
Resistance Drain–Source ($V_{GS} = 4 V, I_D = 100 mA$)	R _{DS(on)}	-	1	2.5	Ω
DYNAMIC CHARACTERISTICS	•		•	•	
Input Capacitance ($V_{DS} = 6 V, V_{GS} = 0, f = 1 MHz$)	C _{iss}	-	14	-	pF
Output Capacitance (V _{DS} = 6 V, V _{GS} = 0, f = 1 MHz)	C _{OSS}	-	11	-	pF
Feedback Capacitance $(V_{DS} = 6 V, V_{GS} = 0, f = 1 MHz)$	C _{rss}	-	1.8	-	pF
FUNCTIONAL CHARACTERISTICS					
Power Gain (V _{DD} = 5.8 Vdc, P _{in} = 20 dBm, I _{DQ} = 150 mA, f = 900 MHz)	G _{ps}	9.5	10	-	dB
Drain Efficiency (V _{DD} = 5.8 Vdc, P _{in} = 20 dBm, I _{DQ} = 150 mA, f = 900 MHz)	ηD	50	55	-	%
Ruggedness Test (V _{DD} = 5.8 Vdc, P _{in} = 20 dBm, I _{DQ} = 150 mA, f = 900 MHz, Load VSWR = 20:1, All Phase Angles at Frequency Test)	Ψ	No De	egradation in Out	put Power afte	er Test

	Table 1. Large Signal Impedance				
	V _{DD} = 5.8 V, F	Pin = 20 dBm, I	DQ = 150 mA		
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f MHz	Z _{in} Ohms	Z _{OL} * Ohms
850	7.0 – j6.4	6.1 – j5.1
900	5.2 – j6.5	5.9 – j4.6
950	5.2 – j6.0	6.1 – j4.7

Z_{OL}* is the conjugate of the optimum load impedance into which the device output operates at a given output power, voltage and frequency.

PACKAGE DIMENSIONS



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