





#### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Features**

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 2)
- ESD Protected up to 2kV
- "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

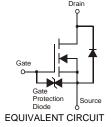
### **Mechanical Data**

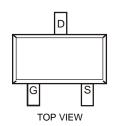
- Case: SOT-523
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.002 grams (approximate)

SOT-523









### **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characterist	ic		Symbol	Value	Units
Drain-Source Voltage			$V_{DSS}$	20	V
Gate-Source Voltage			$V_{GSS}$	±6	V
Continuous Drain Current (Note 1)	Steady State	$T_A = 25$ °C $T_A = 85$ °C	I <sub>D</sub>	0.63 0.45	A
Pulsed Drain Current			I <sub>DM</sub>	6	Α

## Thermal Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 1)	$P_{D}$	0.28	W
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	452	°C/W
Operating and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 to +150	°C

Notes:

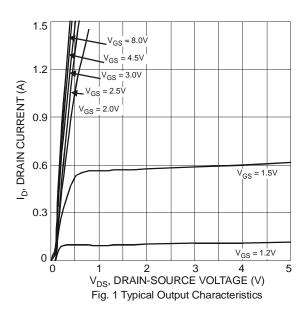
- I. Device mounted on FR-4 PCB.
- No purposefully added lead.
- 3. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.

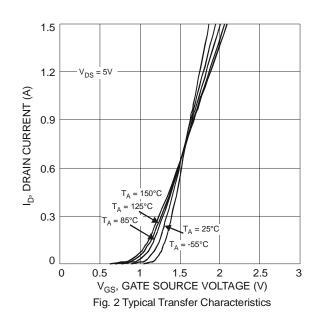


# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

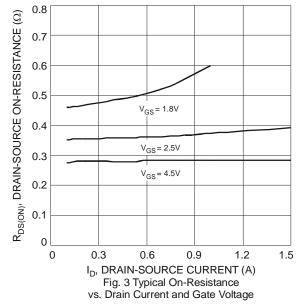
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 4)							
Drain-Source Breakdown Voltage		20	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = 25°C	I <sub>DSS</sub>	-	-	100	nA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	-		±1.0	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 4)							
Gate Threshold Voltage	$V_{GS(th)}$	0.5	-	1.0	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
		-	0.3	0.4	Ω	$V_{GS} = 4.5V, I_D = 600mA$	
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>		0.4	0.5		$V_{GS} = 2.5V, I_D = 500mA$	
	, ,		0.5	0.7		$V_{GS} = 1.8V, I_D = 350mA$	
Forward Transfer Admittance	Y <sub>fs</sub>	-	1.4	ı	S	$V_{DS} = 10V, I_D = 400mA$	
Diode Forward Voltage (Note 4)			0.7	1.2	V	$V_{GS} = 0V, I_{S} = 150mA$	
Diode Forward Voltage (Note 4) $V_{SD}$ 0.7 1.2 $V_{GS}$ = 0V, $I_{S}$ = 150mA <b>DYNAMIC CHARACTERISTICS</b>							
Input Capacitance	C <sub>iss</sub>	-	60.67	i	pF	\	
Output Capacitance	Coss	-	9.68	ı	pF	$V_{DS} = 16V, V_{GS} = 0V,$ of = 1.0MHz	
Reverse Transfer Capacitance	$C_{rss}$	-	5.37		pF	T = T.OWITE	
Total Gate Charge	$Q_g$	-	736.6	-	рC	V 45V V 40V	
Gate-Source Charge	$Q_{gs}$	-	93.6	-	рC	$V_{GS} = 4.5V, V_{DS} = 10V,$	
Gate-Drain Charge	$Q_{gd}$	-	116.6	-	рC	-I <sub>D</sub> =250mA	
Turn-On Delay Time	t <sub>D(on)</sub>	-	5.1	-	ns	101/11/	
Turn-On Rise Time	t <sub>r</sub>	-	7.4	-	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t <sub>D(off)</sub>	-	26.7	-	ns	$R_L = 47\Omega, R_G = 10\Omega,$ $I_D = 200 \text{mA}$	
Turn-Off Fall Time	t <sub>f</sub>	-	12.3	-	ns	TID = ZOUTIA	

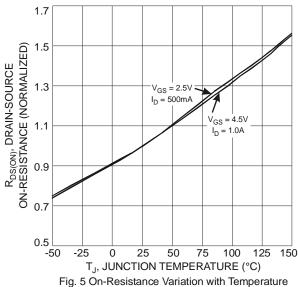
Notes: 4. Short duration pulse test used to minimize self-heating effect.











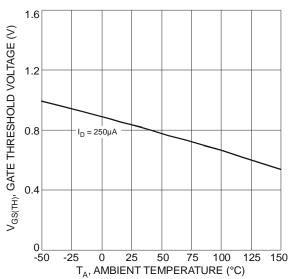


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

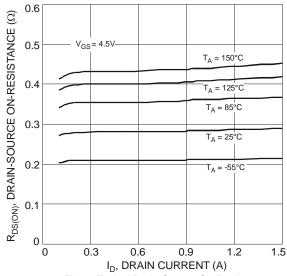


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

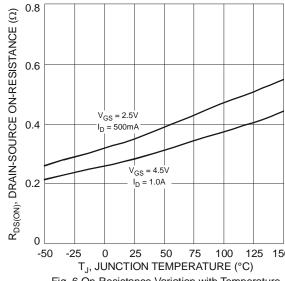


Fig. 6 On-Resistance Variation with Temperature

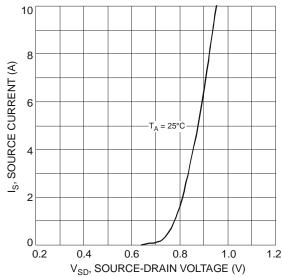
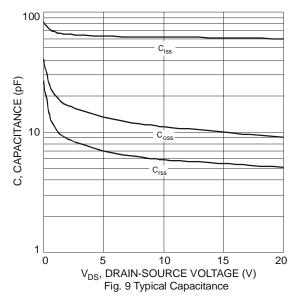


Fig. 8 Diode Forward Voltage vs. Current





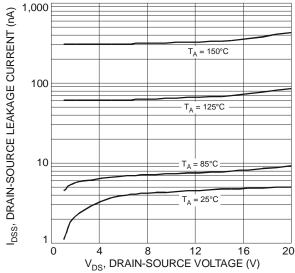


Fig. 10 Typical Drain-Source Leakage Current vs. Drain-Source Voltage

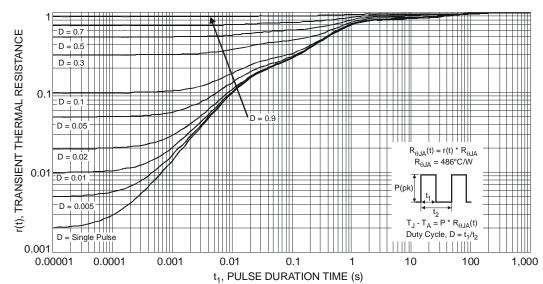


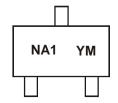
Fig. 11 Transient Thermal Response

# Ordering Information (Note 5)

Part Number	Case	Packaging
DMG1012T-7	SOT-523	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# **Marking Information**



NA1 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: W = 2009)

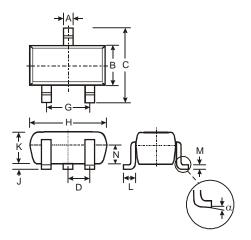
M = Month (ex: 9 = September)

Date Code Key

Year	200	9	2010		2011	20	12	2013		2014	2	2015
Code	W		Х		Υ	2	7	Α		В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

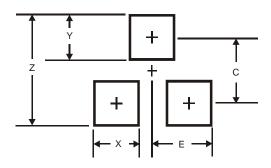


# **Package Outline Dimensions**



	SOT-523						
Dim	Min	Max	Тур				
Α	0.15	0.30	0.22				
В	0.75	0.85	0.80				
С	1.45	1.75	1.60				
D	_	_	0.50				
G	0.90	1.10	1.00				
Н	1.50	1.70	1.60				
J	0.00	0.10	0.05				
K	0.60	0.80	0.75				
L	0.10	0.30	0.22				
M	0.10	0.20	0.12				
N	0.45	0.65	0.50				
α	0°	8°					
All	All Dimensions in mm						

# **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	1.8
Х	0.4
Y	0.51
С	1.3
F	0.7



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